

01-1241

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

DECLARATION FOR PATENT APPLICATION

As the below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled:

METHOD AND APPARATUS PROVIDING CMOS IMAGER DEVICE
PIXEL WITH TRANSISTOR HAVING LOWER THRESHOLD VOLTAGE
THAN OTHER IMAGER DEVICE TRANSISTORS

the specification of which is attached hereto.

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by an amendment, if any, specifically referred to in this oath or declaration.

I acknowledge the duty to disclose all information known to me which is material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code § 119/365 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)			Priority Not Claimed
_____ (Number)	_____ (Country)	_____ (Filing Date)	<input type="checkbox"/>
_____ (Number)	_____ (Country)	_____ (Filing Date)	<input type="checkbox"/>
_____ (Number)	_____ (Country)	_____ (Filing Date)	<input type="checkbox"/>

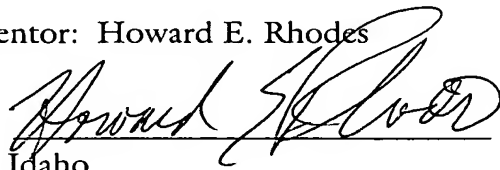
I hereby claim the benefit under Title 35, United States Code, § 120/365 of any United States and PCT international application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56(a) which became available between the filing date of the prior application and the national or PCT international filing date of this application:

_____ (Application No.)	_____ (Filing Date)	_____ (Status) (patented, pending, abandoned)
_____ (Application No.)	_____ (Filing Date)	_____ (Status) (patented, pending, abandoned)
_____ (Application No.)	_____ (Filing Date)	_____ (Status) (patented, pending, abandoned)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Please address all correspondence to Thomas J. D'Amico of **Dickstein Shapiro Morin & Oshinsky LLP** located at 2101 L Street NW, Washington, DC 20037-1526. Telephone calls should be made to (202) 785-9700.

Full name of sole inventor: Howard E. Rhodes

Inventor's signature: 

Date: 12/24/03

Residence: Boise, Idaho

Citizenship: US

Mailing Address: 631 E. Ridgefield Drive
Boise, Idaho 83706

Docket No.: M4065.0624/P624
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Howard Rhodes

Application No.: NOT YET ASSIGNED

Group Art Unit: N/A

Filed: Herewith

Examiner: Not Yet Assigned

For: METHOD AND APPARATUS
PROVIDING CMOS IMAGER DEVICE
PIXEL WITH TRANSISTOR HAVING
LOWER THRESHOLD VOLTAGE THAN
OTHER IMAGER DEVICE TRANSISTORS

POWER OF ATTORNEY BY ASSIGNEE AND
CERTIFICATE BY ASSIGNEE UNDER 37 CFR § 3.73(B)

Assistant Commissioner for Patents
Washington, DC 20231

Dear Sir:

Micron Technology, Inc., Assignee of the entire right, title and interest in the above-identified application by virtue of the Assignment attached hereto (which is also being submitted concurrently for recordation), hereby appoints the attorneys and agents of the firm of Dickstein Shapiro Morin & Oshinsky LLP located at 2101 L Street NW, Washington, DC 20037-1526, listed as follows:

All practitioners at Customer Number 24998.

and also, listed as follows:

All practitioners at Customer Number 24998 Charles B. Brantley, III, 38,086; Michael L. Lynch, 30,871; Kevin D. Martin, 37,882; David J. Paul, 34,692; and Russ Slifer, 39,838.

Application No.: NOT YET ASSIGNED

Docket No.: M4065.0624/P624

attorneys of Micron Technology, Inc. as its attorneys with full power of substitution to prosecute this application and to transact all business in the Patent and Trademark Office in connection therewith.

The Assignee certifies that the above-identified assignment has been reviewed and to the best of the Assignee's knowledge and belief, title is in the assignee.

Please direct all correspondence regarding this application to the following:

Thomas J. D'Amico
DICKSTEIN SHAPIRO MORIN & OSHINSKY LLP
2101 L Street NW
Washington, DC 20037-1526
Telephone: (202) 785-9700
Fax: (202) 887-0689

Respectfully submitted,

MICRON TECHNOLOGY, INC.



Michael L. Lynch
Chief Patent Counsel
Registration No. 30,871

Dated: Dec 5, 2003

ASSIGNMENT AND AGREEMENT


For value received, I, Howard E. Rhodes, hereby sell, assign and transfer to Micron Technology, Inc., a corporation of the State of Delaware, having an office at 8000 S. Federal Way, Boise, Idaho 83706-9632, U.S.A., and its successors, assigns and legal representatives, the entire right, title and interest, for the United States of America, in and to certain inventions related to an invention entitled METHOD AND APPARATUS PROVIDING CMOS IMAGER DEVICE PIXEL WITH TRANSISTOR HAVING LOWER THRESHOLD VOLTAGE THAN OTHER IMAGER DEVICE TRANSISTORS, described in an application for Letters Patent of the United States, executed by me of even date herewith, and all the rights and privileges in said application and under any and all Letters Patent that may be granted in the United States for said inventions; and I also concurrently hereby sell, assign and transfer to Micron Technology, Inc. the entire right, title and interest in and to said inventions for all countries foreign to the United States, including all rights of priority arising from the application aforesaid, and all the rights and privileges under any and all forms of protection, including Letters Patent, that may be granted in said countries foreign to the United States for said inventions.

I authorize Micron Technology, Inc. to make application for such protection in its own name and maintain such protection in any and all countries foreign to the United States, and to invoke and claim for any application for patent or other form of protection for said inventions, without further authorization from me, any and all benefits, including the right of priority provided by any and all treaties, conventions, or agreements.

I hereby consent that a copy of this assignment shall be deemed a full legal and formal equivalent of any document which may be required in any country in proof of the right of Micron Technology, Inc. to apply for patent or other form of protection for said inventions and to claim the aforesaid benefit of the right of priority.

I request that any and all patents for said inventions be issued to Micron Technology, Inc. in the United States and in all countries foreign to the United States, or to such nominees as Micron Technology, Inc. may designate.

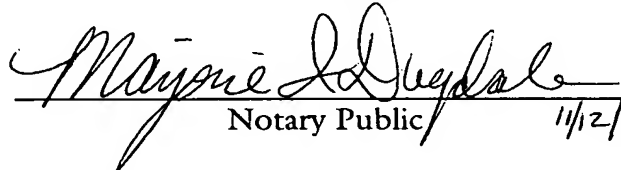
I agree that, when requested, I shall, without charge to Micron Technology, Inc. but at its expense, sign all papers, and do all acts which may be necessary, desirable or convenient in connection with said applications, patents, or other forms of protection.


Howard Rhodes

Date: 12/24/03

United States of America)
State of Idaho) ss.:
County of Ada)

On this 24th day of December, 2003, before me personally came Howard Rhodes, to me known to be the individual described in and who executed the foregoing instrument, and acknowledged execution of the same.


Notary Public 11/12/2009

